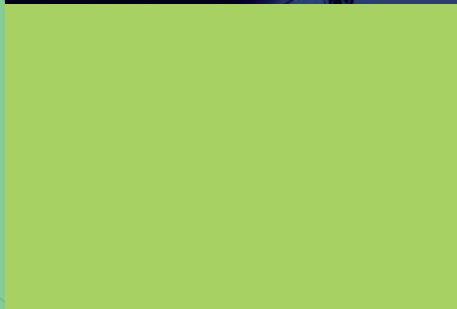


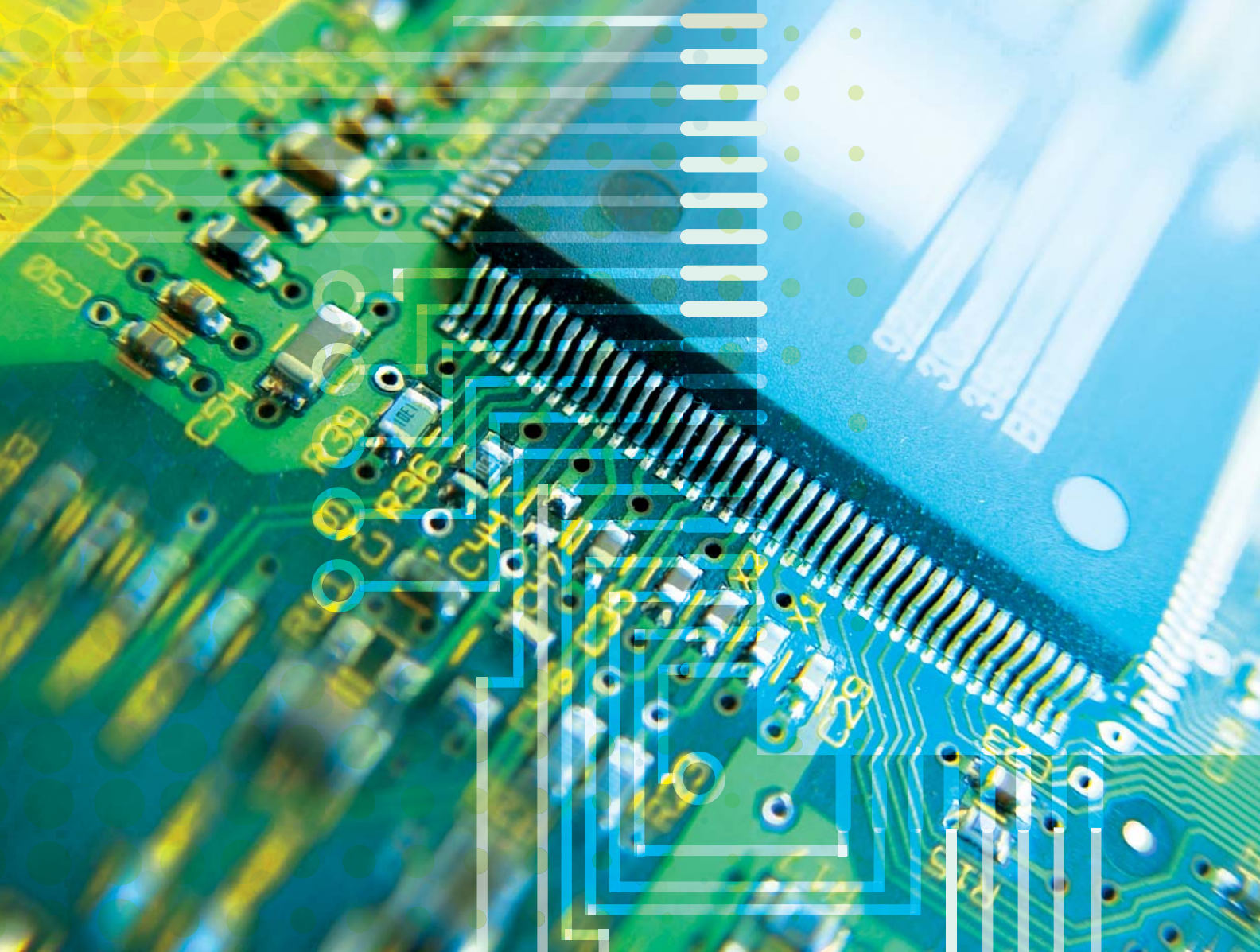


Working together and setting the Research and Development  
agenda for nanoelectronics in Europe.

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Nanoelectronics represent the key technology and innovation required for advancing a sustainable ICT economy. Nanoelectronic components are embedded in various systems surrounding us in our daily life. They allow for: less energy consumption, more efficient and sustainable generation of energy, enhanced healthcare and new health applications, environmental monitoring, carbon friendly production, safer and sustainable transport, electric vehicles, better food control, ambient assisted living, better security, and many more.

Europe has demonstrated its capability to create innovation with major success stories such as GSM, automotive electronics, smart cards, lithography equipment and silicon on insulator (SOI) just to name a few.

AENEAS aims at strengthening these capabilities and it also builds on new opportunities by bringing together people and ideas in order to set the Research and Development agenda for nanoelectronics in Europe. Its work is complemented by the funding tool ENIAC JU. By working together and defining a common strategy, Europe will lead in the further miniaturisation and integration of devices, while dramatically increasing their functionalities.

**Marcel Annegarn**  
**General Director of AENEAS**





## GOALS AND OBJECTIVES

AENEAS is a non-profit industrial association established in 2006; it federates European R&D players: large industrial companies, Small and Medium Enterprises (SMEs), and research organisations.

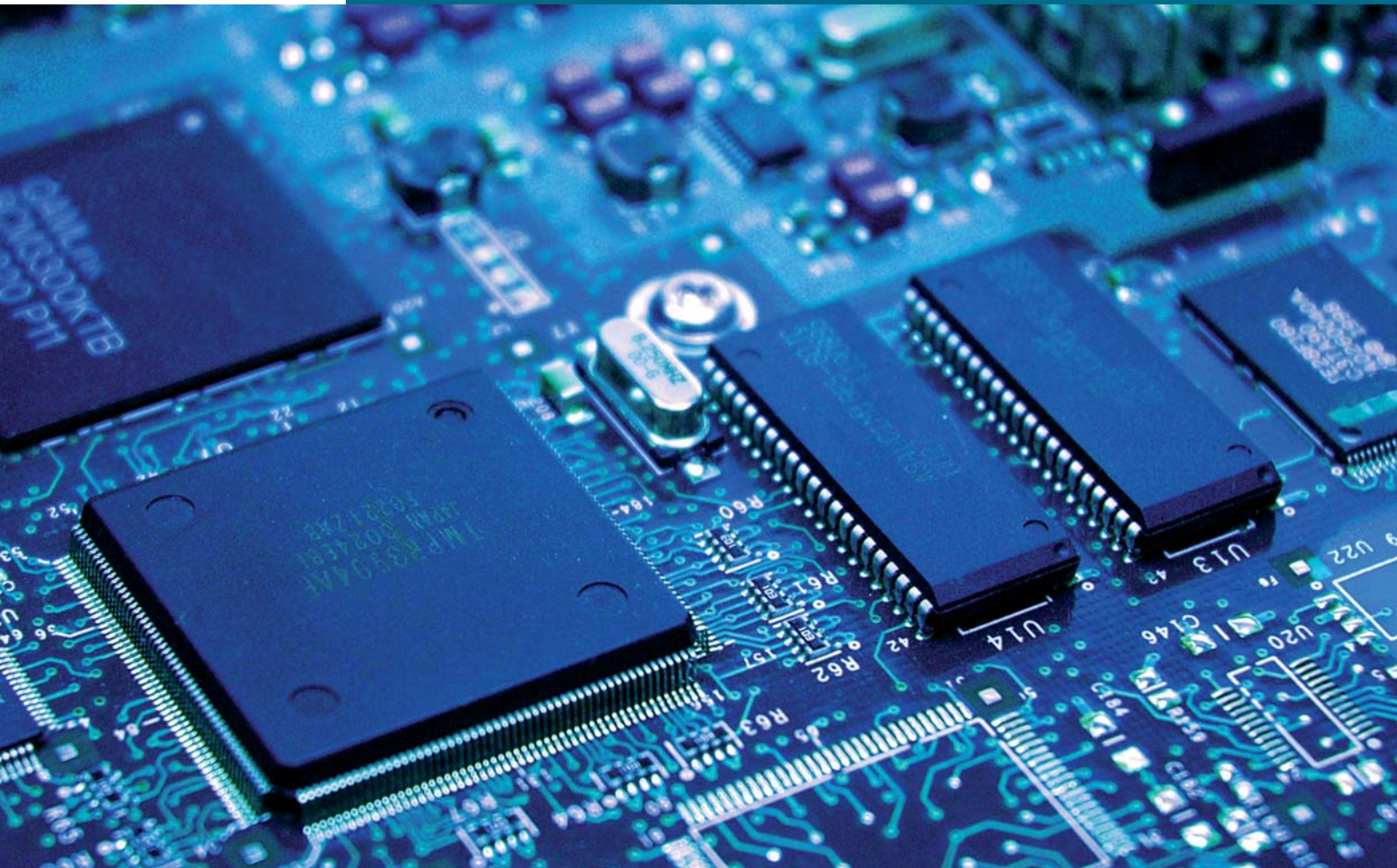
It is the meeting place for the European nanoelectronics community.

AENEAS represents the R&D actors in the ENIAC Joint Undertaking, and participates to two third of its operational budget; The ENIAC Joint Undertaking is the leading instrument that funds European R&D activities in nanoelectronics.



## AENEAS

- Sets the R&D agenda on nanoelectronics in Europe by being the voice for nanoelectronics.
- Manages access to pan European R&D funding
- Brings people and ideas together to run R&D projects



## WORK AREAS

Created through the concerted efforts of European experts from industry, academia and research organisations with the participation of public authorities, the strategic agendas identify important societal needs and lead markets, translated into priorities for each of the core technology domains of the nanoelectronics research environment. The work areas include:

### Applications

1. Automotive and Transport
2. Communications and Digital Lifestyle
3. Energy Efficiency
4. Health and Ageing Society
5. Safety and Security

### Technologies

6. Design Technologies
7. Semiconductor Process and Integration
8. Equipment, Materials and Manufacturing

### The strategic agendas consist of:

- The Annual Work Plan (AWP),
  - The Vision, Mission, Strategy (VMS),
- which can all be found on [www.aeneas-office.eu](http://www.aeneas-office.eu)

### History:

ENIAC Technology Platform was launched in 2004 to enhance the competitiveness of the European nanoelectronics industry. It brought together the key players in the field to develop a common roadmap with the aim of optimising the effectiveness of investments in R&D.

The roadmap was successfully defined and presented in the Strategic Research Agenda (SRA). Part of the implementation of the SRA involved the creation of two legal entities:

- The ENIAC Joint Undertaking, a public private partnership to carry out R&D projects
- The Association of European Nanoelectronics Activities (AENEAS).







## MEMBERSHIP BENEFITS

- Build up your network
- Integrate a European community of experts in your field
- Form highly competent consortia with the benefit of shared expertise
- Have a say in policy making
- Benefit from a close connection to the Scientific Community Council (SCC)
- Enjoy facilitated access to European R&D funding programmes

Become a member on [www.aeneas-office.eu](http://www.aeneas-office.eu)



SMEs are a rich source of Innovation and Technical Expertise and are a great added value to the European R&D ecosystem





## THE STRUCTURE

AENEAS members are the driving force behind the research strategy which outlines the basis of future project calls. Through a consultation of all its members and partners, the association defines the key objectives of the industry in order to ensure European competitiveness.

There are several working groups, amongst others, working on updating the strategic documents. Members are encouraged to take part in these working groups and in the General Assembly meetings in order to vote on key decisions and elect candidates to the Board.

AENEAS is organised in 3 chambers: one for SMEs, one for Universities and Research organisations, and one for large Corporations. Associated Members are natural persons, organisations, or public research organisations active in the field of nanoelectronics not carrying out R&D activities. The 3 chambers are represented in the AENEAS Steering Board which oversees the association's administration and management. The 15 members of the Steering Board are appointed by the General Assembly. From its members, the Steering Board name a Presidium: the President and two Vice Presidents as well as a Secretary and Treasurer.

To technically advise the Board, a Support Group meets frequently throughout the year to propose solutions and closely follow each development. On a daily basis, AENEAS is managed by a small office, led by the General Director, who reports to the President of the Association.



